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Using ADL together to define XXXG4SimEvent

... Now the ADL files should have been generated: XXXHit.adl #ifndef XXXHit_ADL #define XXXHit_ADL ... @Title: Module ADL generator for Together" @Author ...
www.physik.uni-muenchen.de/~schiefer/adltogether.html - 25k - [Cached](#) - [Similar pages](#)

Technical Annex 2 to SWA 2001/01: Data Dictionary

... Data Object Adapter Generator Deployed: Prototype Reverse Engineering
(C++) ADL Generator. April 2002: ADL Reflection API Deployed ...
www.usatlas.bnl.gov/computing/imgmt/ datadict-annex-07.Ju01.html - 15k - [Cached](#) - [Similar pages](#)

pon Minutes of the DD meeting: LAPP - 09/02/2001-02-12

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... ContainedObject or ObjectVector<- do we define the type of the transient object inheritance in the description or - as an "ADL generator" option Not sure ...
atlas.web.cern.ch/.../SOFTWARE/OO/architecture/DataDictionary/Meetings/DD_Group/2001/Minutes_090201.pdf - Supplemental Result - [Similar pages](#)

pon Athena Data Dictionary and Description Language

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... Reverse Engineering (C++) ADL Generator — A code generator which parses declarative C++ (ie header-like C++) and produces compatible ADL ...
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Page 1. THE FLORIDA STATE UNIVERSITY COLLEGE OF ARTS AND SCIENCES CUSTOMIZABLE LEARNING OBJECTS METADATA AUTHORIZING BY ELENA VALENTINA ...
eid.lib.fsu.edu/theses/availableId-08282003-172442/ unrestricted/01_evm_thesis.pdf - [Similar pages](#)

pon 소프트웨어공학 국제과제 개발 현황 및 방향

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... (PSM Refiner). 코드 생성기. (Code Generator). 아키텍처 구축기. ADL 생성기. (ADL Generator). 아키텍처 모델 편집기. (Architecture Model Editor). ...
seibb.cateagu.ac.kr/conference/2002_KSEJW/paper/Technology.ppt - Supplemental Result - [Similar pages](#)

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ppn HDL Gen: Architecture Description Language driven HDL Generation ...

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... OPCode add (OP_TYPE DATA_OP) (OPERANDS (SRC1 int_n) (SRC2 int_32) (DST int_rf ...
result

of an analysis and optimization process of the HDL generator HGEN, the ...
www.cecs.ucd.edu/technical_report/R03-04.pdf - Similar pages

ppn Outline Motivation Architecture Description Language (ADL) The ...

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... Compiler Synthesis Feedback (Area, Power, Clock Frequency) HDL Generator Hardware
Model ... 21 ADL Specification of the DLX Processor (OPCODE OPCODE ADD ADD ...
www.rsp-workshop.org/history/slide03/session-10-p3.pdf - Similar pages

ppn MetaCore An Application Specific DSP Development System

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... nectivity synthesis In the macro block generation step the HDL generator generates
each ... is used for the pipeline design in this model the opcode is transitted ...
www.sigda.org/Archives/ProceedingArchives/Dac/Dac98/papers/1998/dac98.pdf/files/48_2.pdf - Similar pages

ppn lisa

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... (bit[s] OPcode, short operandA, short operandB,); ... This enables the HDL
generator to generate the appropriate HDL code from a more abstract specification. ...
www.cse.iitd.ernet.in/esproj/cthomepage/docs/seminars/naku/asset_group.ppt - Similar pages

ppn Relargetable Compilers

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Embedded Systems Design Workshop, IIT Delhi, Jan 2002 © R. Leupers, Relargetable
Compiler Technology, Rainier Leupers, Aachen University of Technology ...
www.cse.iitd.ernet.in/esproj/workshop-2002/speakers/rainer/slides/rtl_tutorial.ppt - Similar pages

ppn A RETARGETABLE TOOL-SUITE FOR THE DESIGN OF APPLICATION SPECIFIC ...

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... assembler and linker, but significant work was not reported on the HDL generator. ... 3.
Instruction mode operands (OPERAND) 4. Instruction mode opcode (CODING) 5 ...
www.sci.usc.edu/~asyed/1622-ISCAS.pdf - Similar pages

ppn A Methodology for the Design of Application Specific Instruction ...

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... which forward information about the instruction such as instruction opcode, operand
registers ... which are in a subsequent phase of the HDL generator run filled ...
https://www.covare.com/pls/cst_portal/docs/PAGE/COVAREDOCUMENT/PDFtechpaper_aachen3.PDF - Similar pages

ppn C/C : A Retargetable Tool-Suite for Embedded Processors

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... June 2003 • G O : an HDL generator that generates a synthesisable VHDL model ... constraints
often occur in application-specific processors, to save opcode space ...
www.retarget.com/doc/rtarget-whitepaper.pdf - Similar pages

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www.PfizerforWomen.com/
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ppn FAQA 18, 1996 hotline@sd.com

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... 1. When I invoke Visual HDL I receive the following message : Invalid Opcode 88 ... if
I forget to select my synthesis tool in Options->HDL Generator ->Target when ...
product-support.web.cern.ch/product-support/doc/Visual/Visual_doc/mvs_faq.pdf - Similar pages

ppn A Methodology for Validation of Microprocessors using Equivalence ...

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... Different Verify Failed Success RTL Design Hardware Model HDL Generator Generic Models ... operation
is then described in terms of it's opcode, operands, behavior ...
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